

SFP+

Amphenol SFP+ Optical Transceivers are high performance, high density solutions for 10Gb Ethernet and Fibre Channel applications. These transceivers are multi-rate devices allowing for a wider range of applications and backward compatibility to legacy Fibre Channel specifications. The transceivers feature a high reliability 850nm vertical-cavity surface-emitting laser (VCSEL) allowing for flexible link distances up to 300m. The module supports the latest Improved Pluggable FormFactor specification for improved EMI performance.

Interface

- > 20 pin board-as-connector
- > Duplex-LC Optical connector

Cable Options

- > OM2, OM3

Key Features

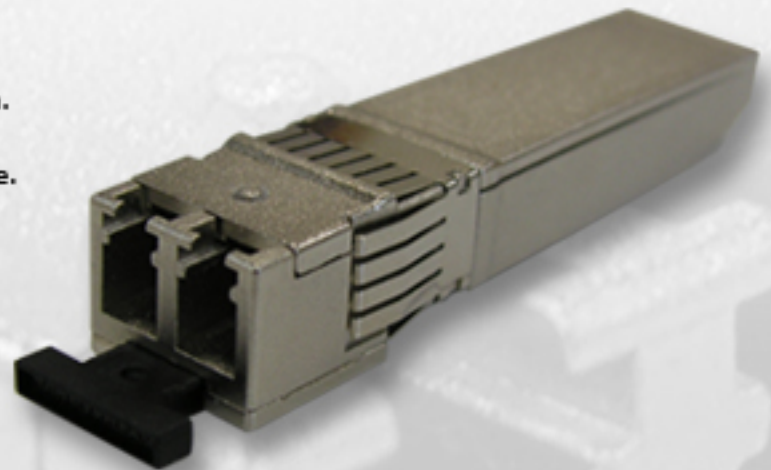
- > I/O Connector designed for high speed differential signal applications
- > Improved Pluggable FormFactor (IPF) compliant for enhanced EMI/EMC performance
- > Data rates up to 11.3Gbps, backward compatible to 1Gbps
- > Support for 1x, 2x, 4x, and 8x Fibre Channel data rates
- > Link Distances up to 300m
- > High Reliability and Low Power Consumption
- > Push-to-release latch mechanism
- > Compliant to SFP+, SFF-8431

Applications

- > High capacity I/O in Storage Area Networks, Network Attached Storage, and Storage Servers
- > Switched fabric I/O such as ultra high bandwidth switches and routers
- > Data center cabling infrastructure
- > High density connections between networking equipment

10 Gigabit Ethernet

Optical Module



Gigabit Ethernet products
fc@aipce.com

Serial Data Rates

Standard	Baud Rate (Gb/s)
8.5G FC	8.5
10GbE (802.3ea)	10.3125
10GbE (802.3eq)	10.3125
10G FC	10.51
802.3ea with FEC	11.3

Connector Standard

SFF-8083, 0.8m SFP+ Card Edge Connector

Supported Standards

SFF-8431, Small FormFactor Pluggable '+'

SFF-8432, Improved FormFactor Pluggable

SFF-8472, Monitoring Interface

Supported Link Distance

		B/W (850nm)	10GBASE-SR 850nm VCSEL
OM1	62.5µm core, 200MHz.km (nominal)	160MHz.km	26m
		200MHz.km	33m
OM2	50µm core, 500MHz.km (nominal)	400MHz.km	66m
		500MHz.km	82m
OM3	Laser Optimized 50µm core, 2000MHz.km	1500MHz.km	300m

Signal Pin Out Description

Pin	Logic	Symbol	Name/Description
1		VeeT	Transmitter Ground
2	LV-TTL-O	TX_Fault	Transmitter Fault
3	LV-TTL-I	TX_DIS	Transmitter Disable
4	LV-TTL-I/O	SDA	Two Wire Serial Data
5	LV-TTL-I	SCL	Two Wire Serial Clock
6		MOD_DEF0	Module present, connect to VeeT
7	LV-TTL-I	RS0	N/A
8	LV-TTL-O	LOS	Loss of Signal
9	LV-TTL-I	RS1	N/A
10		VeeR	Receiver Ground
11		VeeR	Receiver Ground
12	CML-O	RD-	Receiver Data Inverted
13	CML-O	RD+	Receiver Data Non-Inverted
14		VeeR	Receiver Ground
15		VccR	Receiver Supply 3.3V
16		VccT	Transmitter Supply 3.3V
17		VeeT	Transmitter Ground
18	CML-I	TD+	Transmitter Data Non-Inverted
19	CML-I	TD-	Transmitter Data Inverted
20		VeeT	Transmitter Ground